

INFORMATION DISCLOSURE
CITATION IN AN
APPLICATION

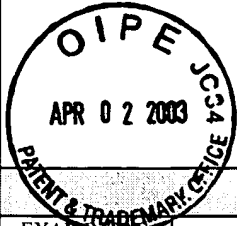
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APPLICANT
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2818 2188



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U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
<i>R</i>	6,131,145	10/10/2000	Matsubara et al.	711	137	

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>R</i>	"Reducing Memory Penalty by a Programmable Prefetch Engine for On-Chip Caches" Tien-Fu Chen <i>Microprocessors and Microsystems</i> V.21 No. 2 (Oct. 1, 1997) pp. 121-130.
<i>R</i>	"SCIMA: Software Controlled Integrated Memory Architecture for High Performance Computing" Masaki Kondo et al., International Conference on Computer Design in Austin, TX (Sept. 17-20, 2000) pp.105-111
<i>R</i>	"SCIMA: A Novel Processor Architecture for High Performance Computing" Masaki Kondo High Performance Computing in the Asia-Pacific Region http://ieeexplore.ieee.org/ie15/6804/18265/00846477.pdf (May 14-17, 2000) pp. 355-360

EXAMINER

John M. R.

DATE CONSIDERED

11/6/2003